

GA-M61PME-S2

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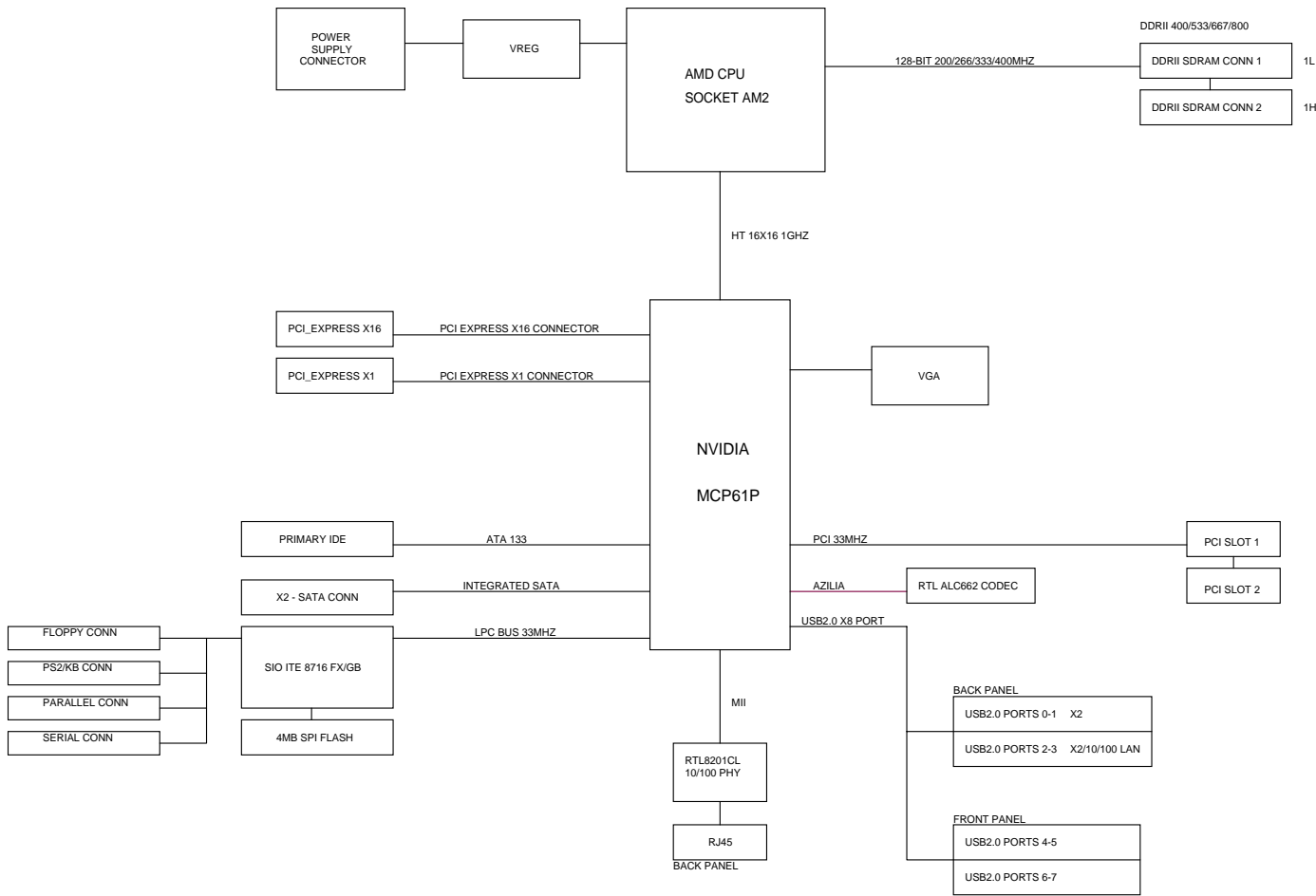
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P-Code: U96133-0

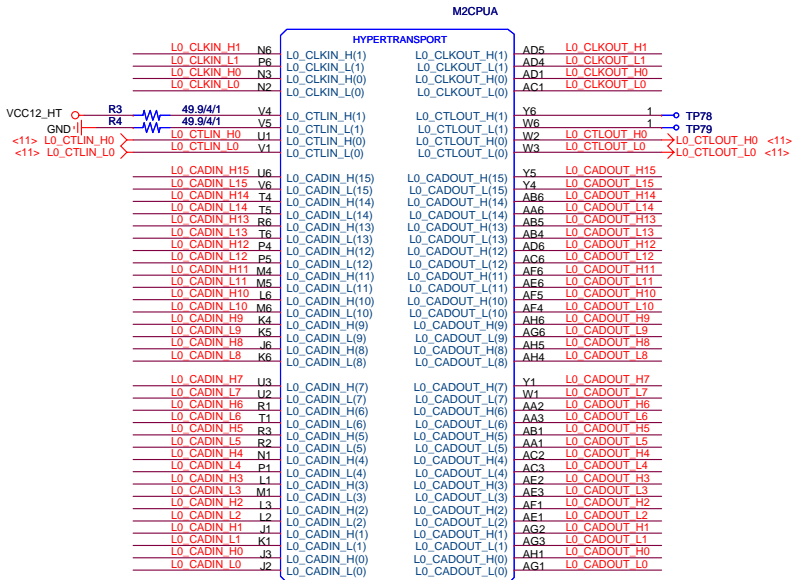
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GIGABYTE				
Title				
BOM & PCB MODIFY HISTORY				
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BLOCK DIAGRAM

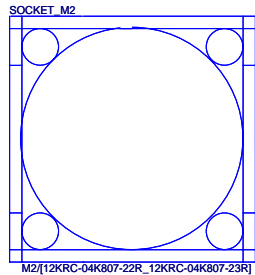


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L0_CADIN_H[0..15] <L0_CADIN_H[0..15] <11>
L0_CLKIN_L[0..1] <L0_CLKIN_L[0..1] <11>
L0_CLKIN_H[0..1] <L0_CLKIN_H[0..1] <11>
L0_CADOUT_L[0..15] <L0_CADOUT_L[0..15] <11>
L0_CADOUT_H[0..15] <L0_CADOUT_H[0..15] <11>
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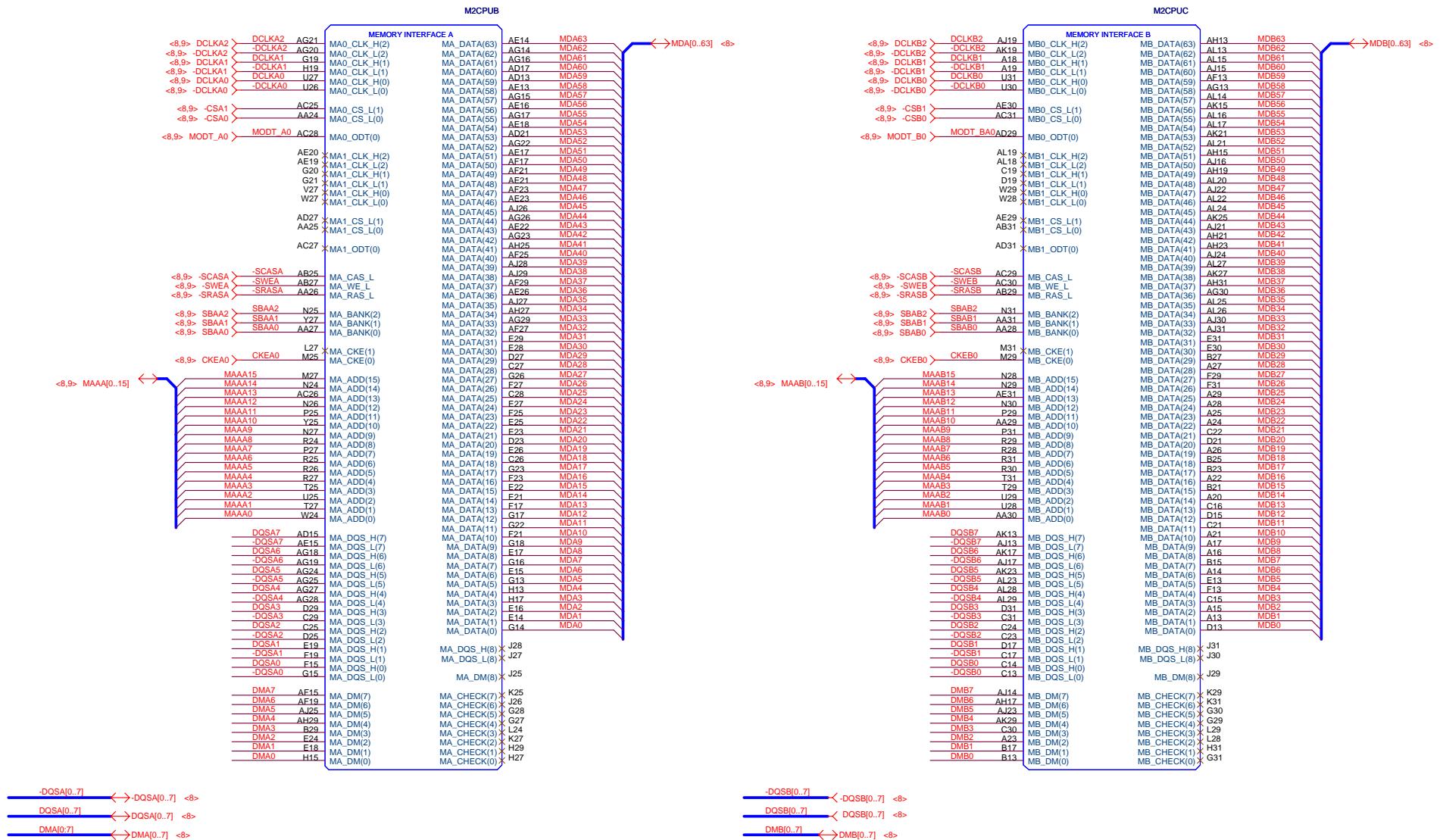
CPU_VDD_RUN = VCORE
CPU_VDDA_RUN = VDDA25
VLDT_RUN = VCC12_HT
CPU_VDDIO_SUS = DDR18V
CPU_VTT_SUS = DDRVTT

VLDT_A = VCC12_HT
VLDT_B = HT12B

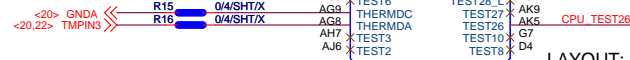
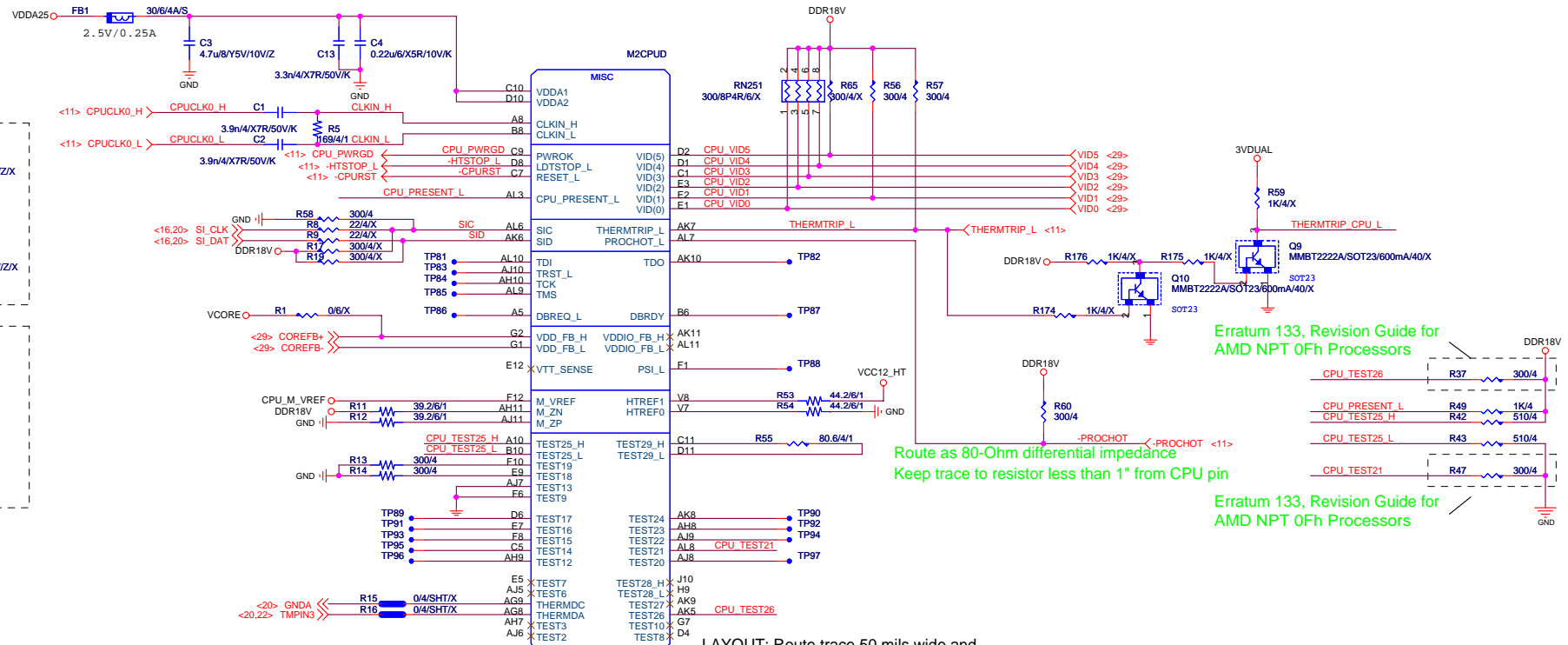


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CPU HYPER TRANSPORT			
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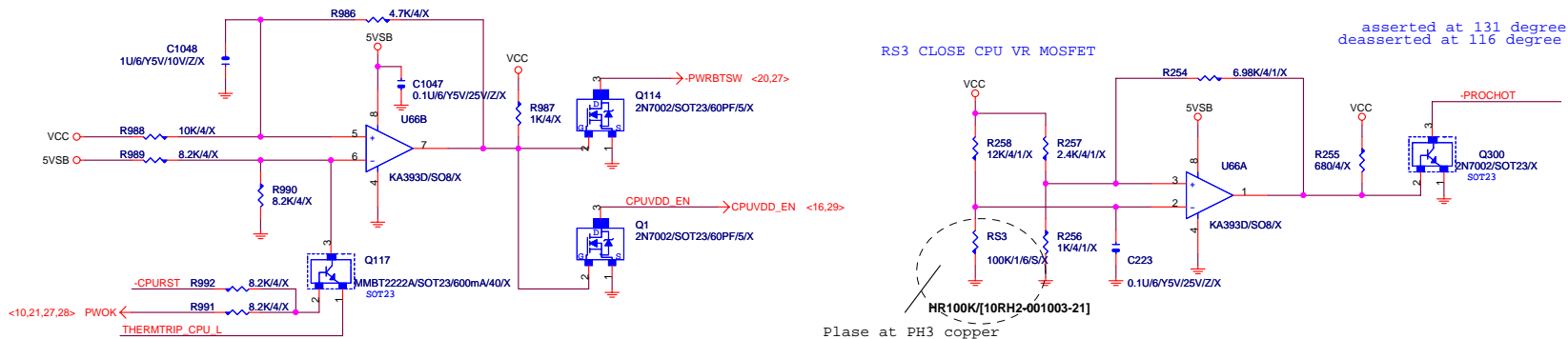
CPU DDRII MEMORY			
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Route as 80-Ohm differential impedance
Keep trace to resistor less than 1" from CPU pin

Erratum 133, Revision Guide for
AMD NPT 0Fh Processors

Erratum 133, Revision Guide for AMD NPT 0Fh Processors



M2CPUE

INTERNAL MISC

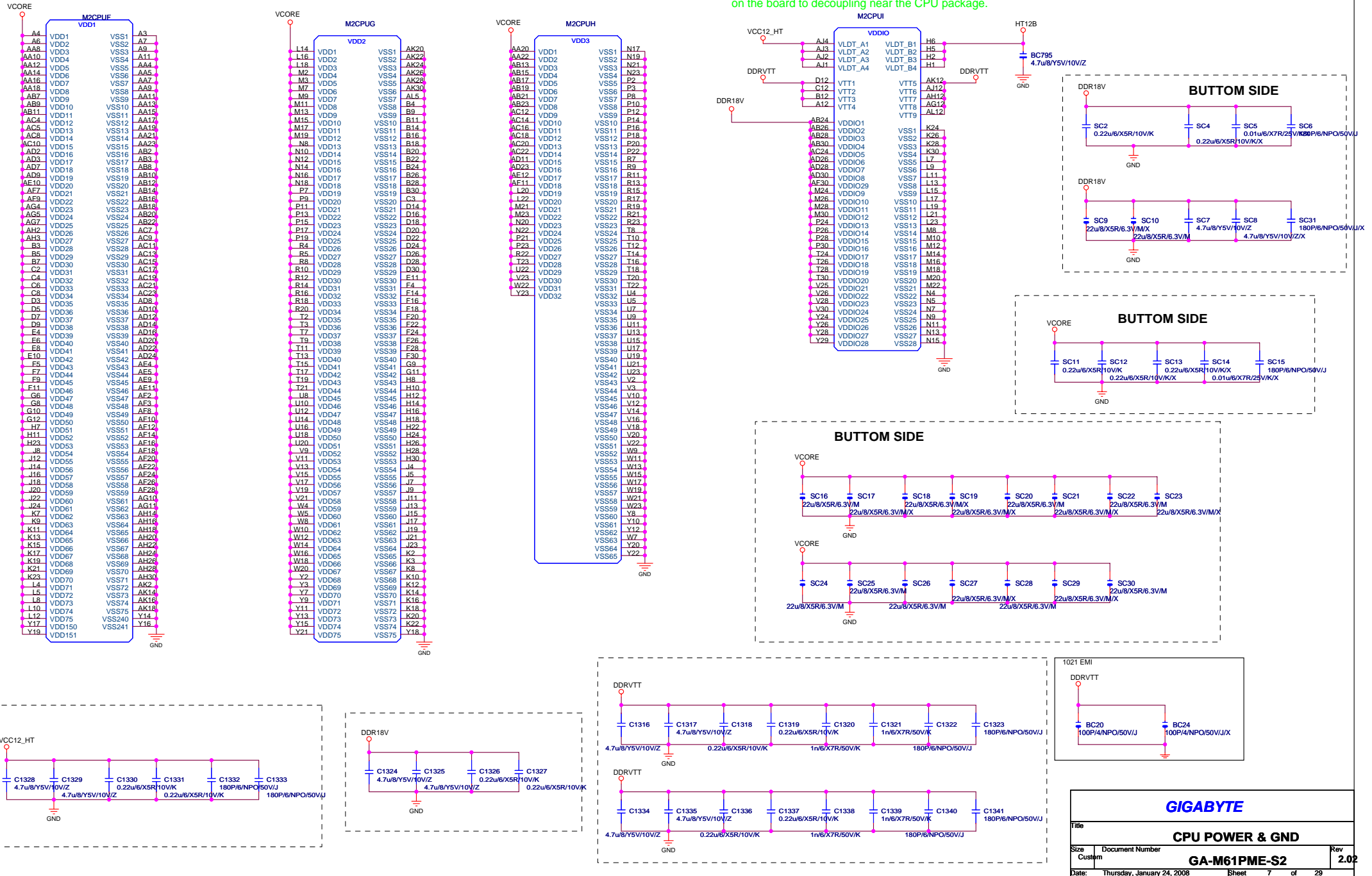
Inputs (Left): L26, L26, L31, L30, W26, W25, AE27, U24, V24, AE28, Y31, Y30, AG31, V31, W31, AF31

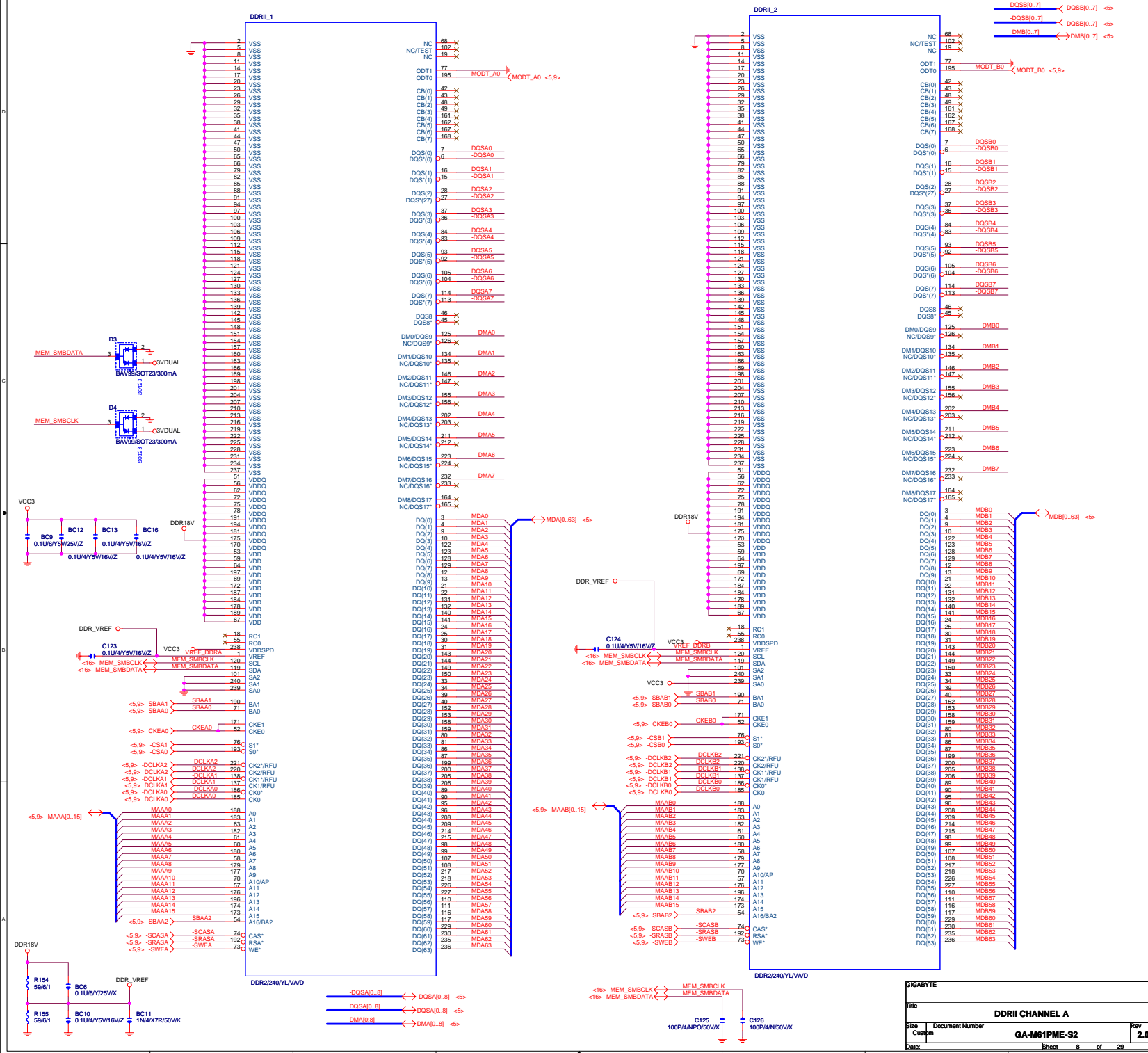
Outputs (Right): E20, B19, AK4, AK3, F2, G4, G3, AD25, AE24, AE25, AJ18, CJ20, C18, G24, G25, G26, G25, W30, W30

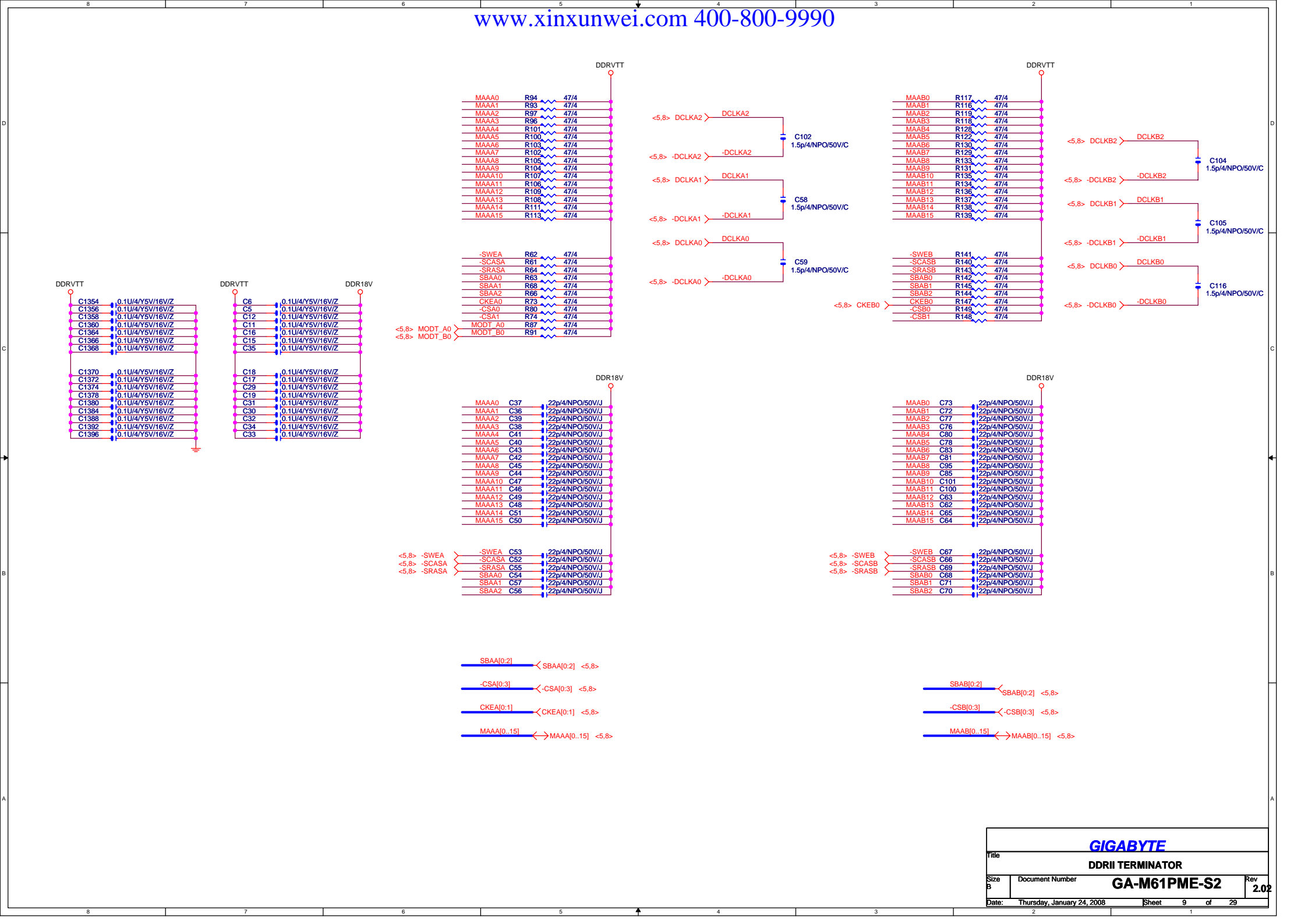
Connections:

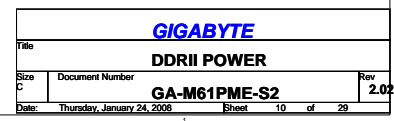
- L26 → RSV01
- L26 → RSV02
- L31 → RSV03
- L30 → RSV04
- W26 → RSV05
- W25 → RSV06
- AE27 → RSV07
- U24 → RSV08
- V24 → RSV09
- AE28 → RSV010
- Y31 → RSV011
- Y30 → RSV012
- AG31 → RSV013
- V31 → RSV014
- W31 → RSV015
- AF31 → RSV016
- RSV017 → E20
- RSV018 → B19
- RSV019 → AK4
- RSV020 → AK3
- RSV021 → F2
- RSV022 → G4
- RSV023 → G3
- RSV024 → AD25
- RSV027 → AE24
- RSV028 → AE25
- RSV030 → AJ18
- RSV031 → CJ20
- RSV032 → C18
- RSV033 → G24
- RSV034 → G25
- RSV035 → G26
- RSV036 → G25
- RSV037 → W30
- RSV038 → W30

VLDT_RUN_B is connected to the VLDT_RUN power supply through the package or on the die. It is only connected on the board to decoupling near the CPU package.

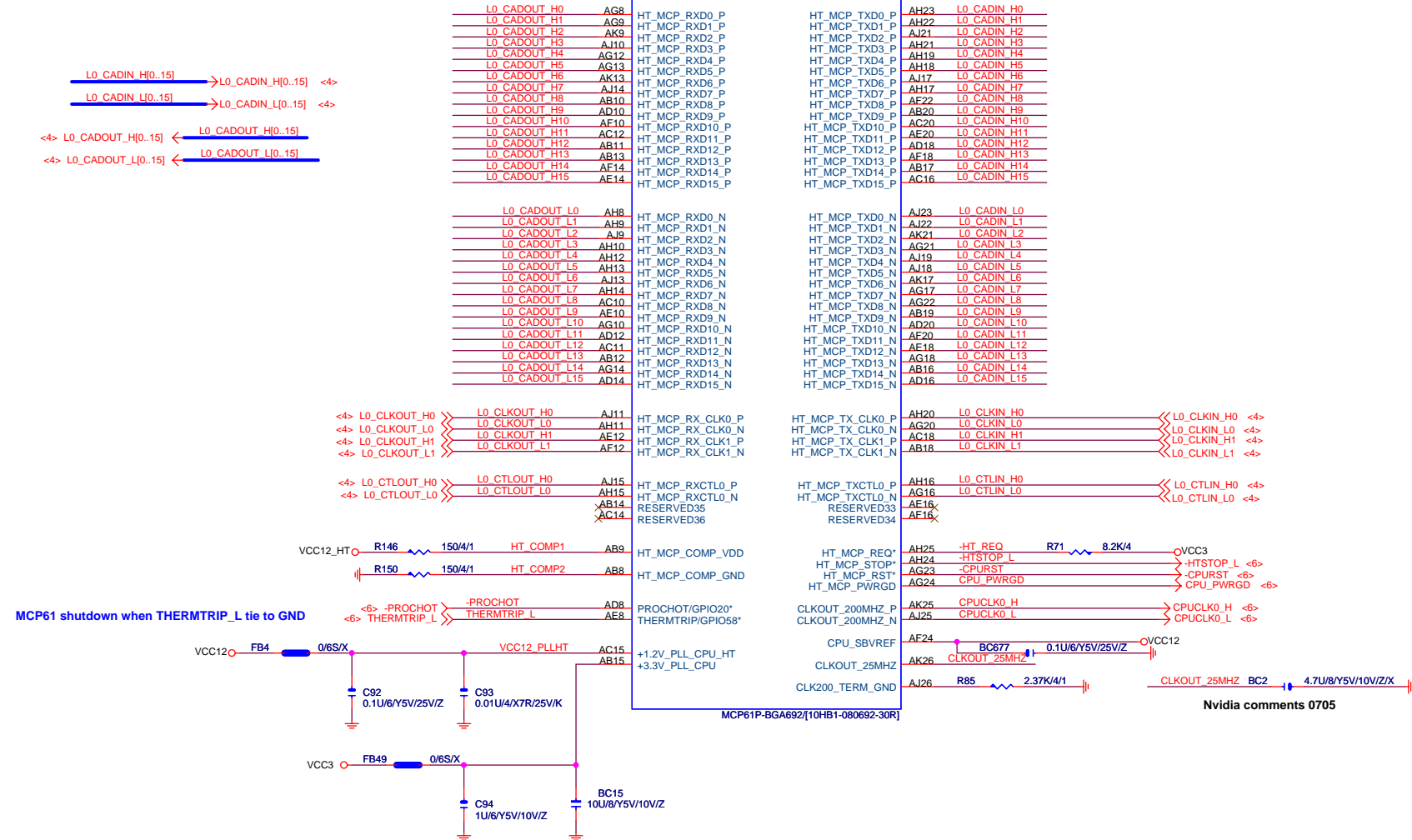






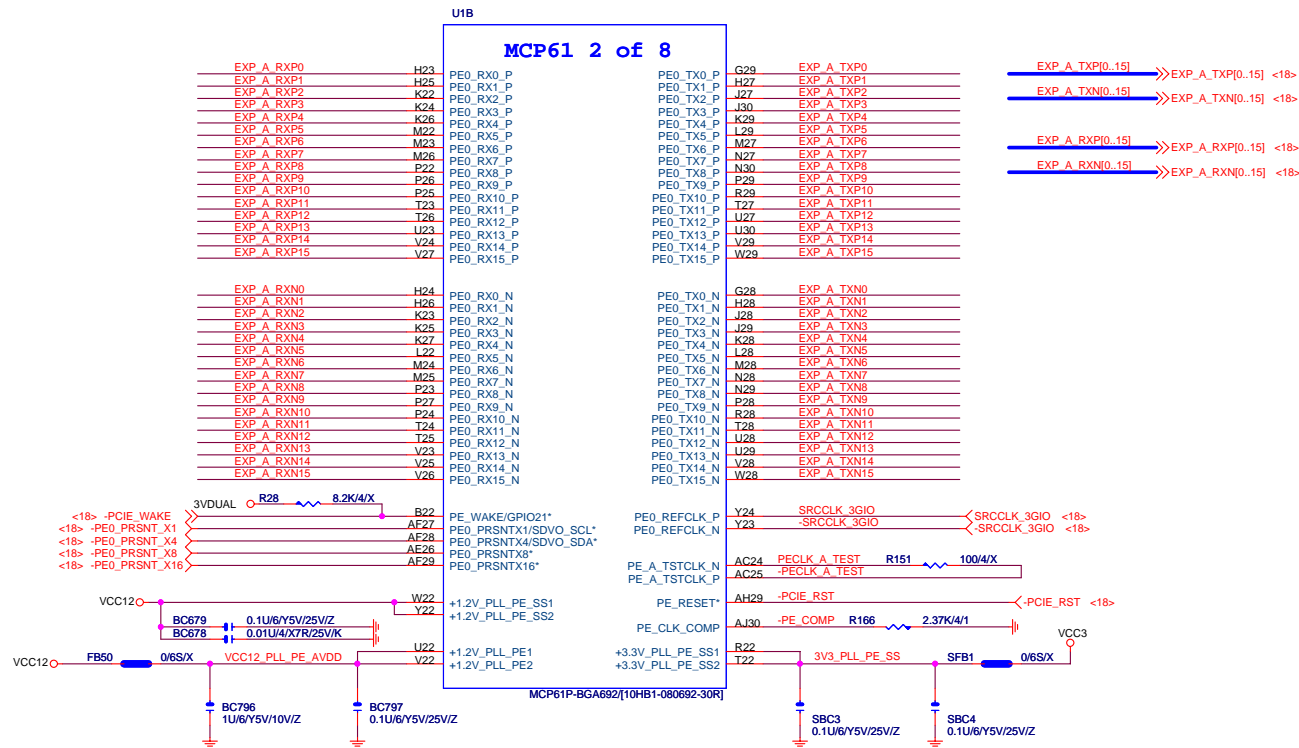


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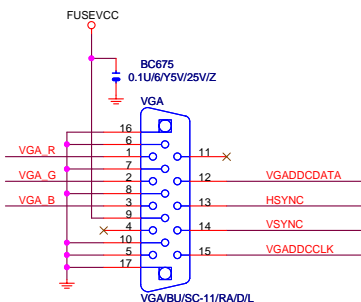
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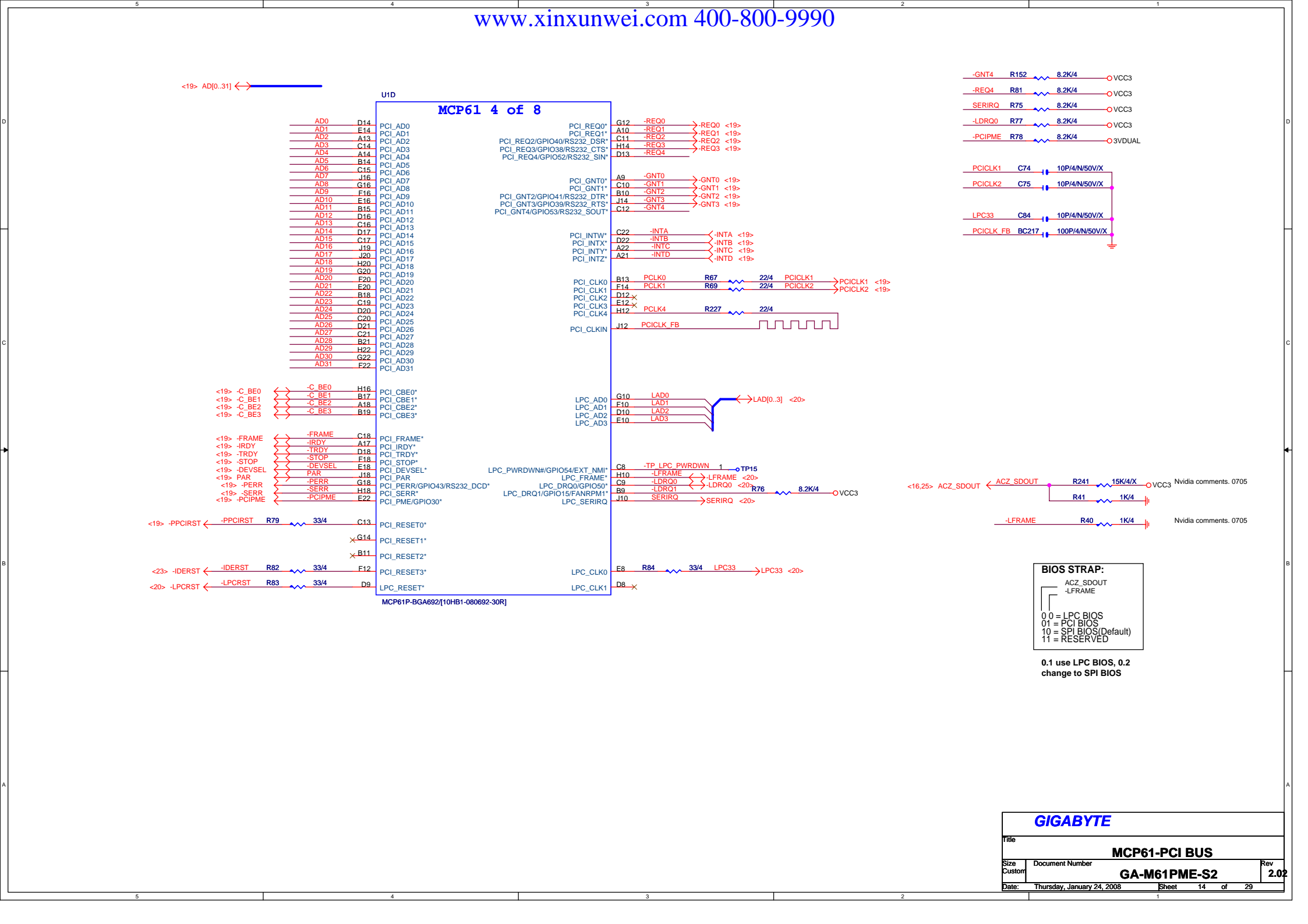
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GA-M61PME-S2			
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Custom			
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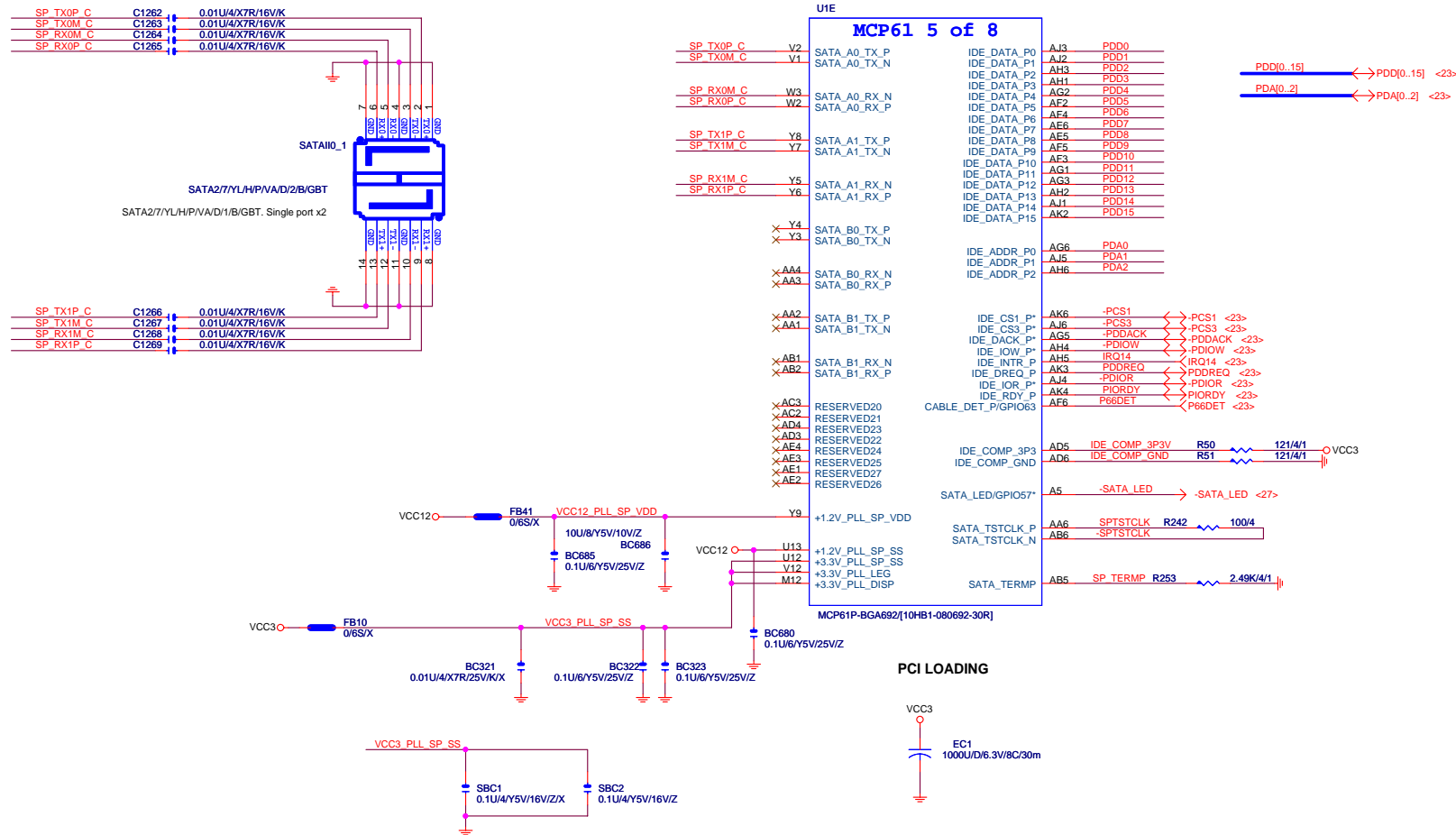


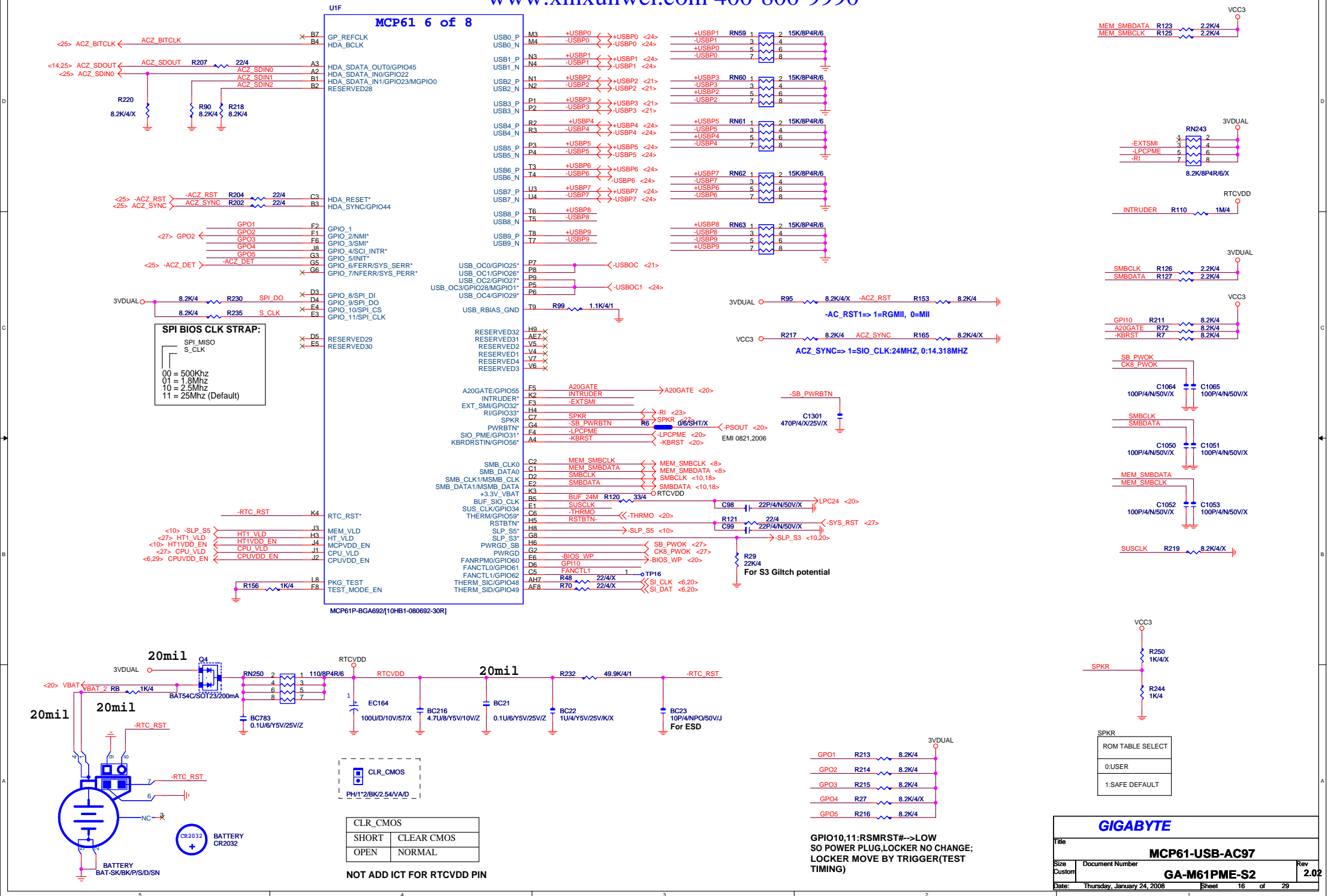
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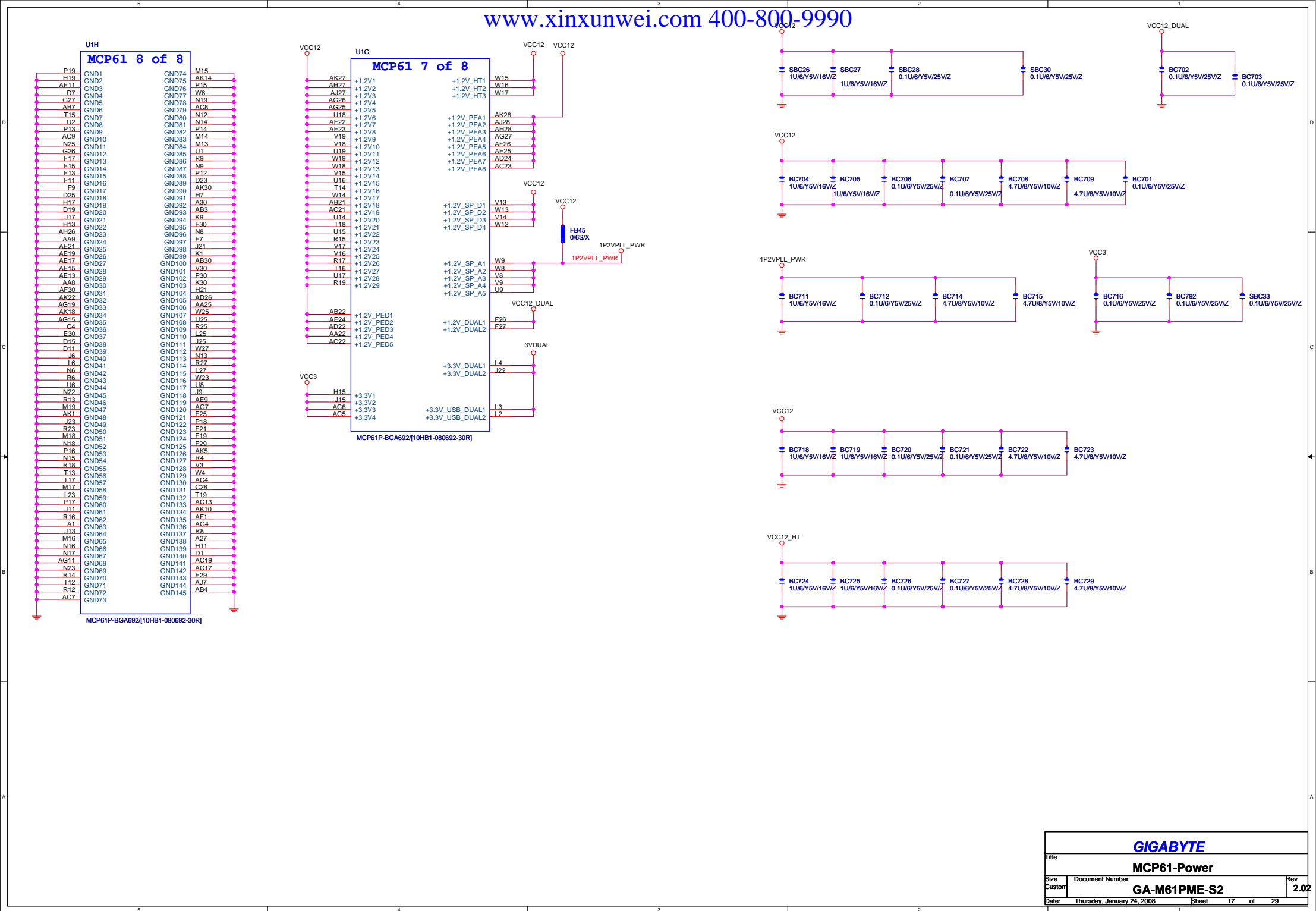
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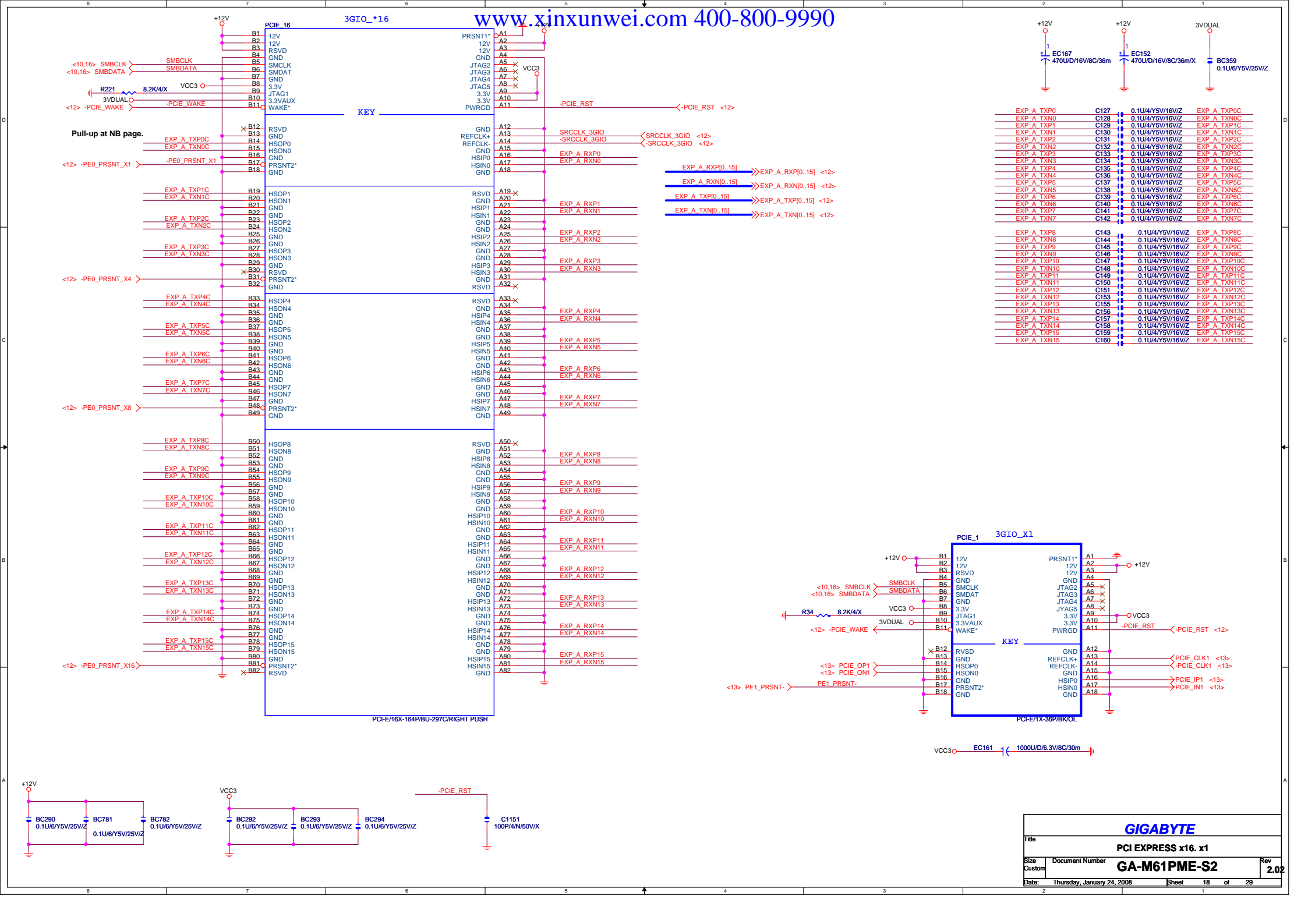


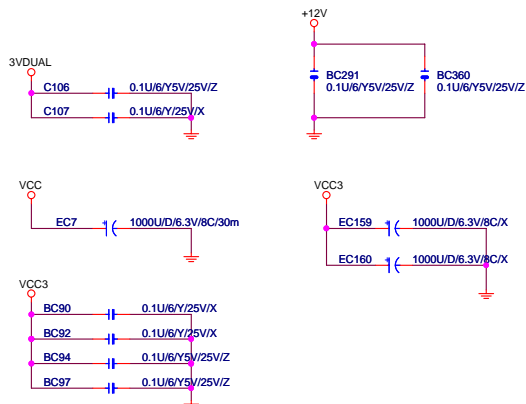
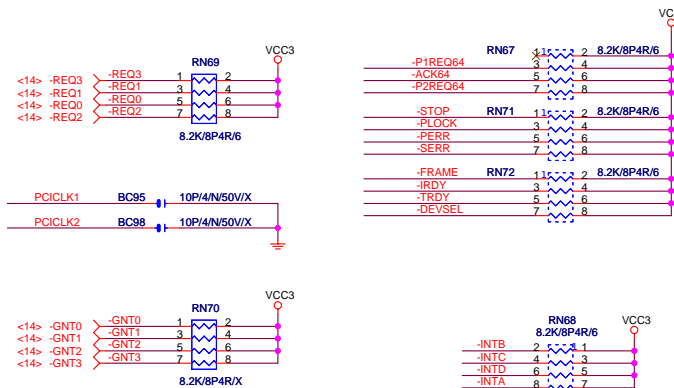
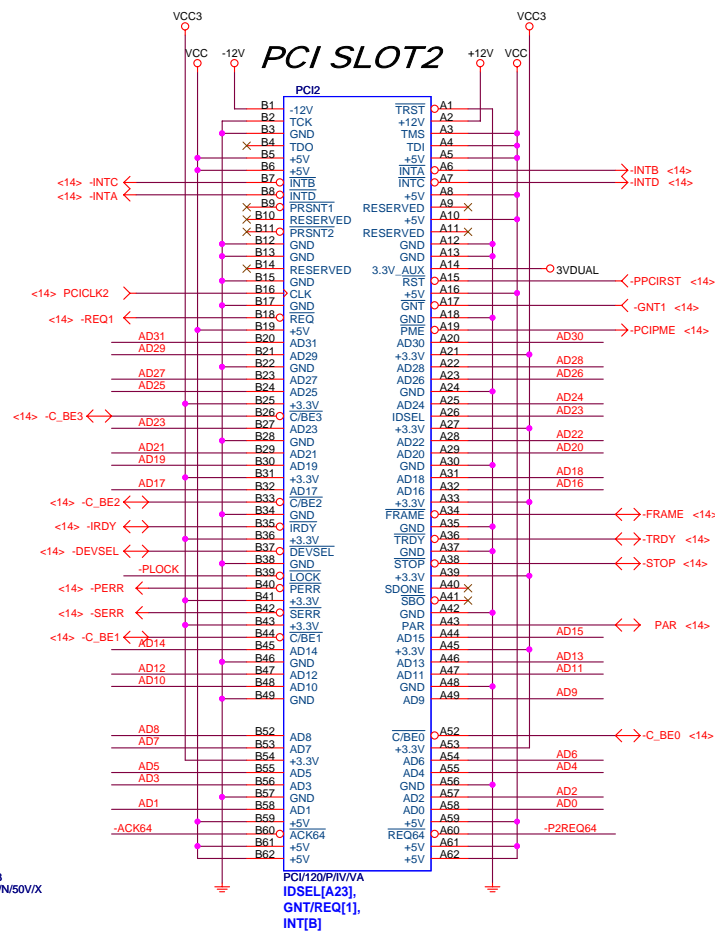
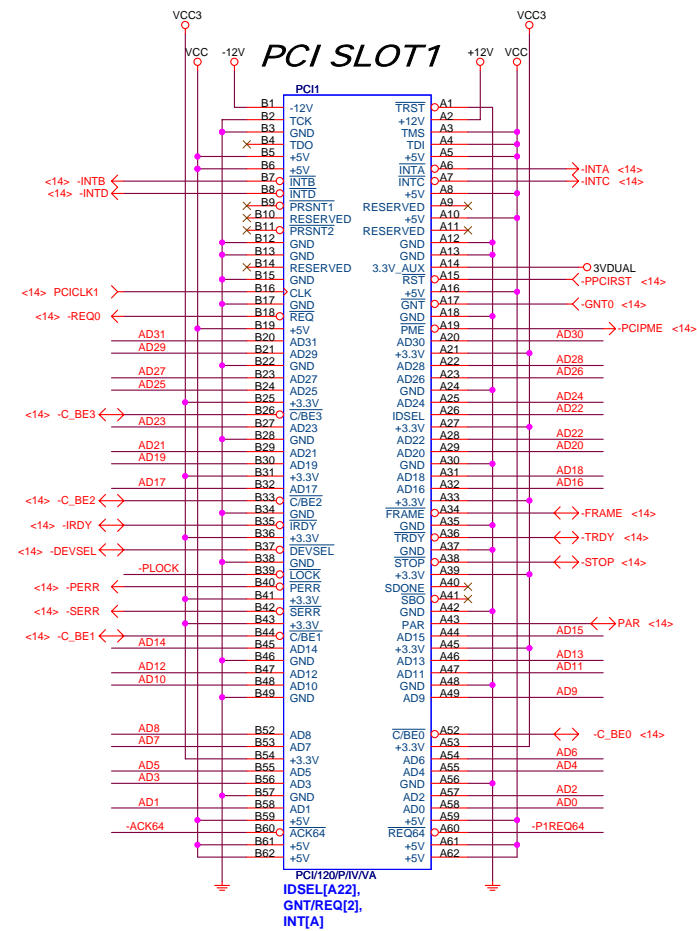


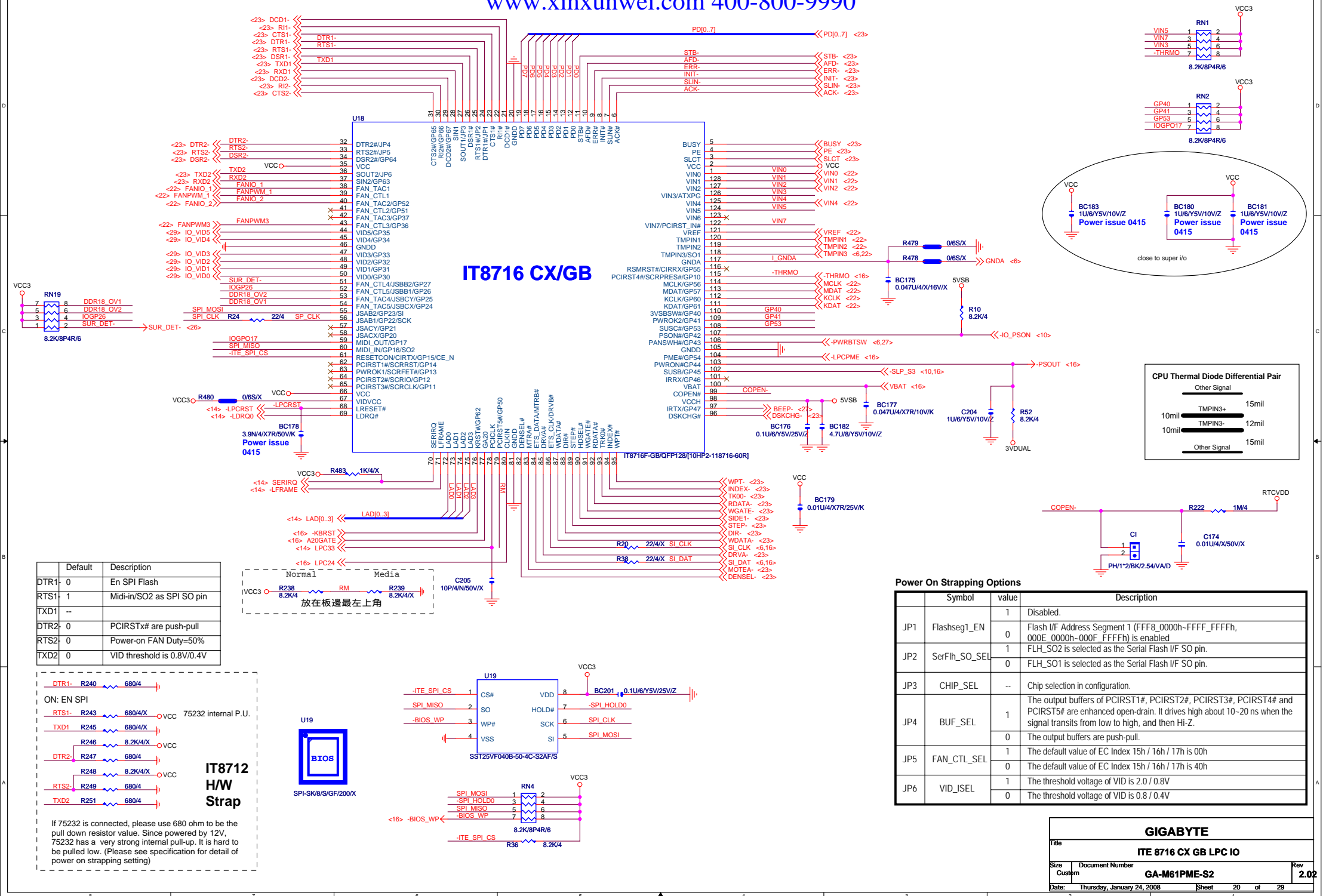


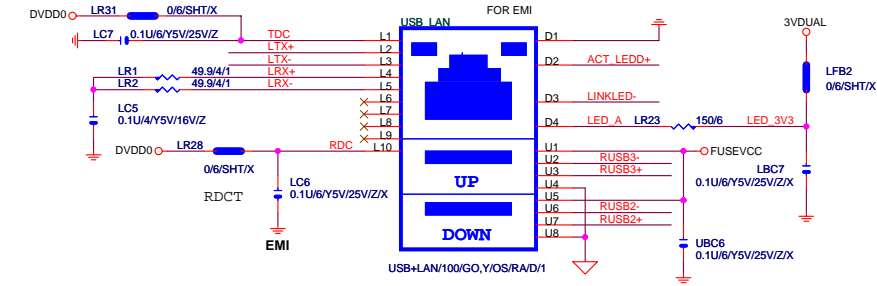
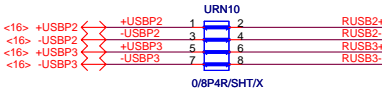
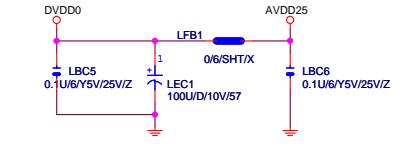
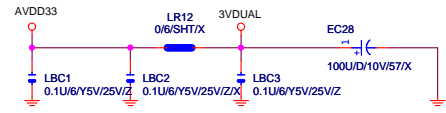
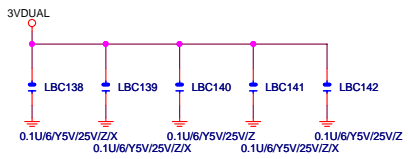
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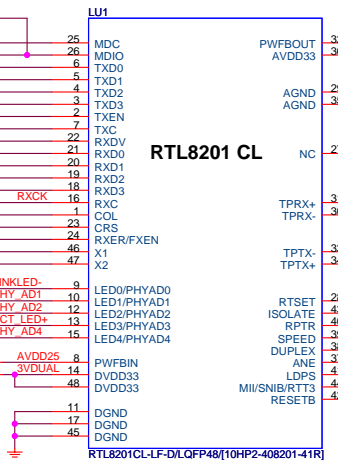
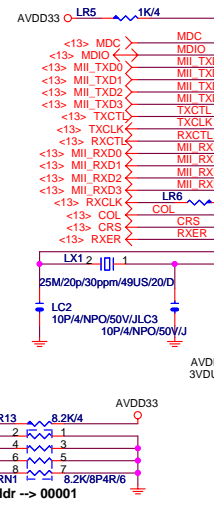




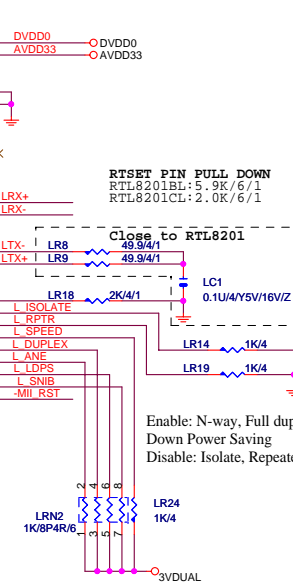
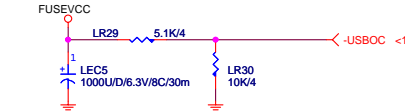
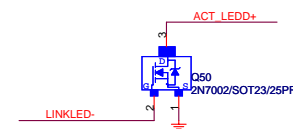
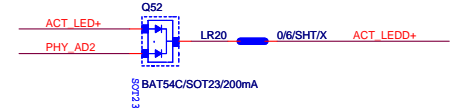




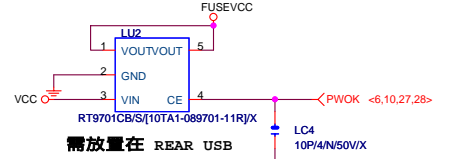
MDIO nvidia recommend pull-up to 3VDUAL



RTL8139C+ should configure to MDux = 0, MLink=1
MDupActiveState = 0 MLinkActiveState = 1

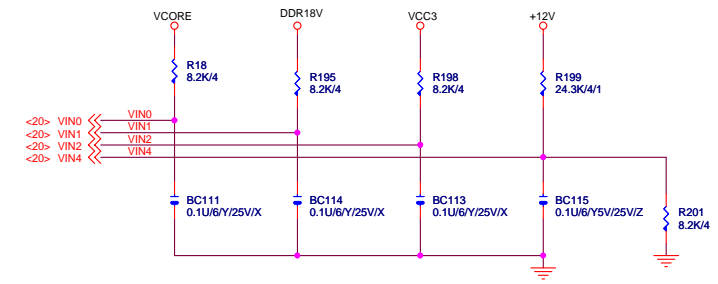
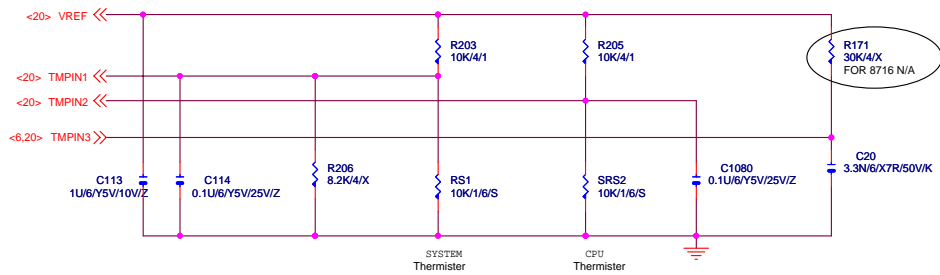


Enable: N-way, Full duplex, 100Mbps, Link
Down Power Saving
Disable: Isolate, Repeater Mode

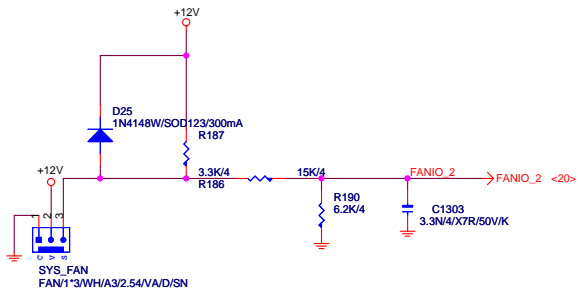


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RTL 8201CL			
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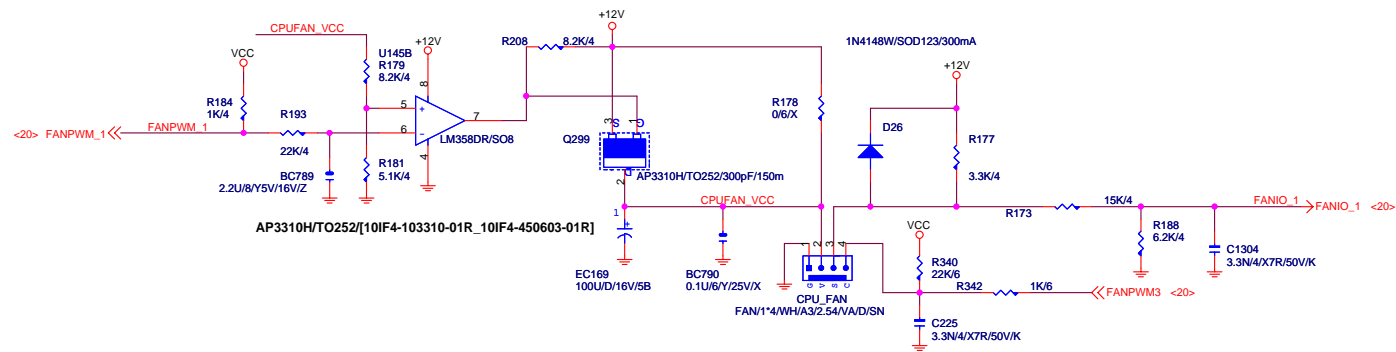
Hardware Monitor circuits



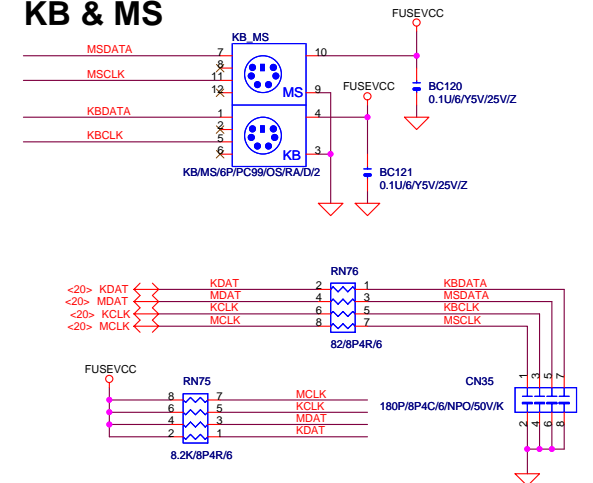
SYSTEM FAN

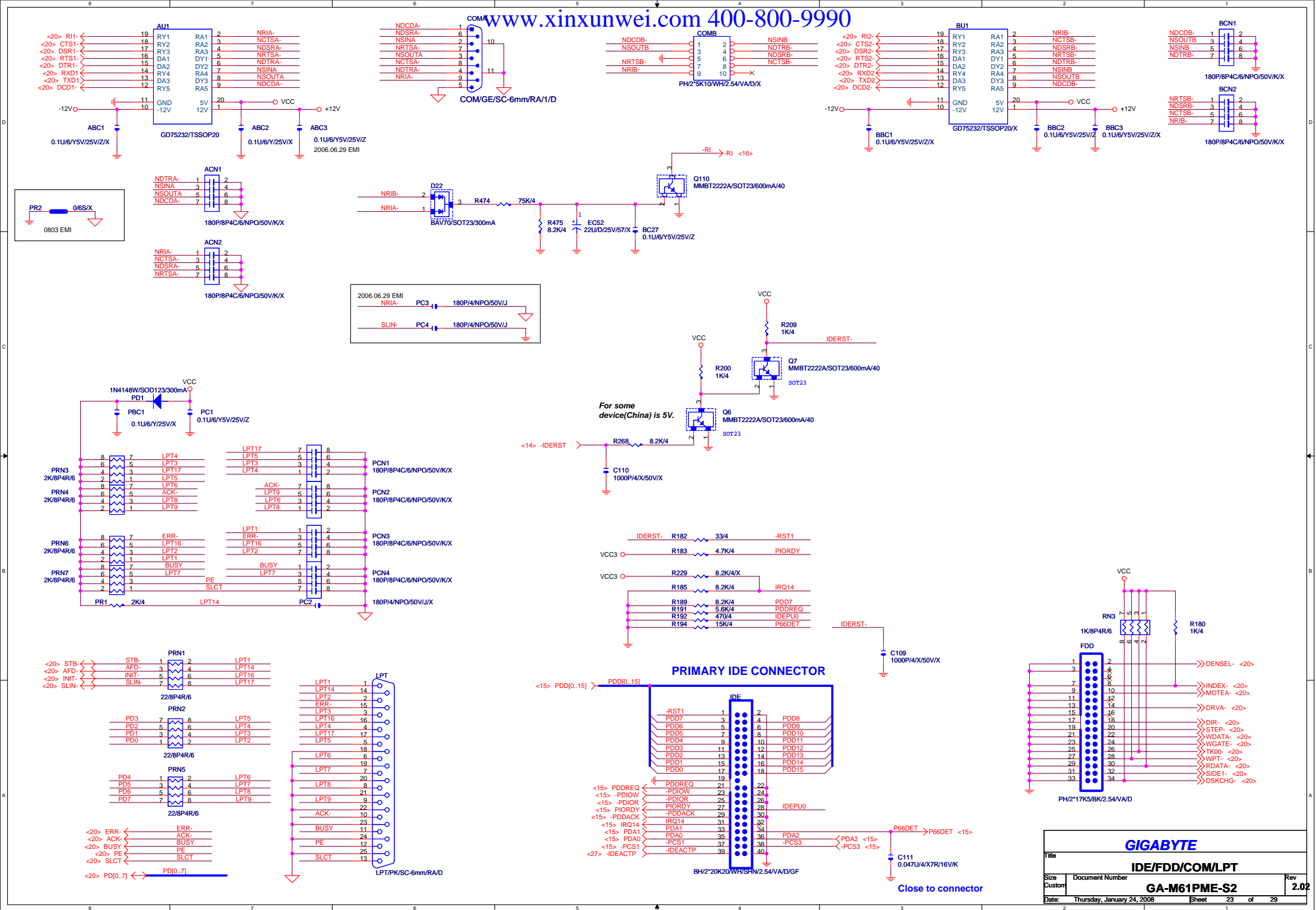


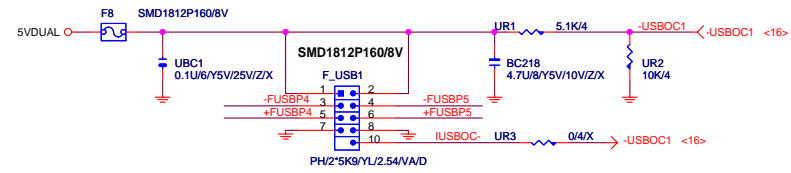
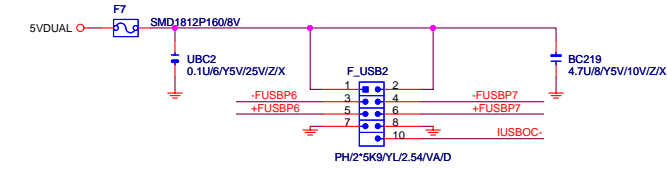
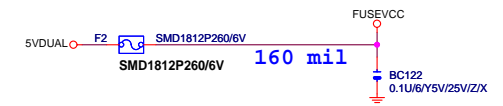
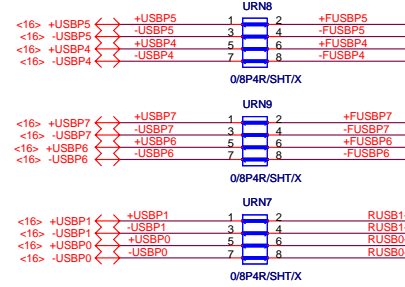
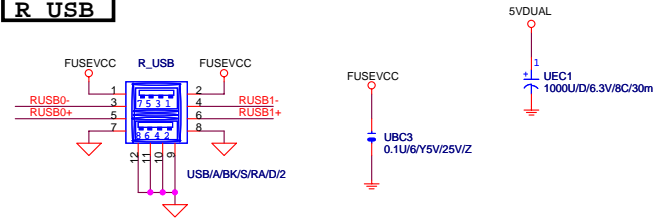
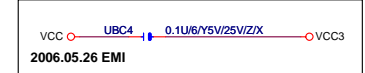
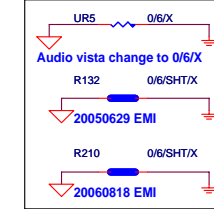
CPU FAN

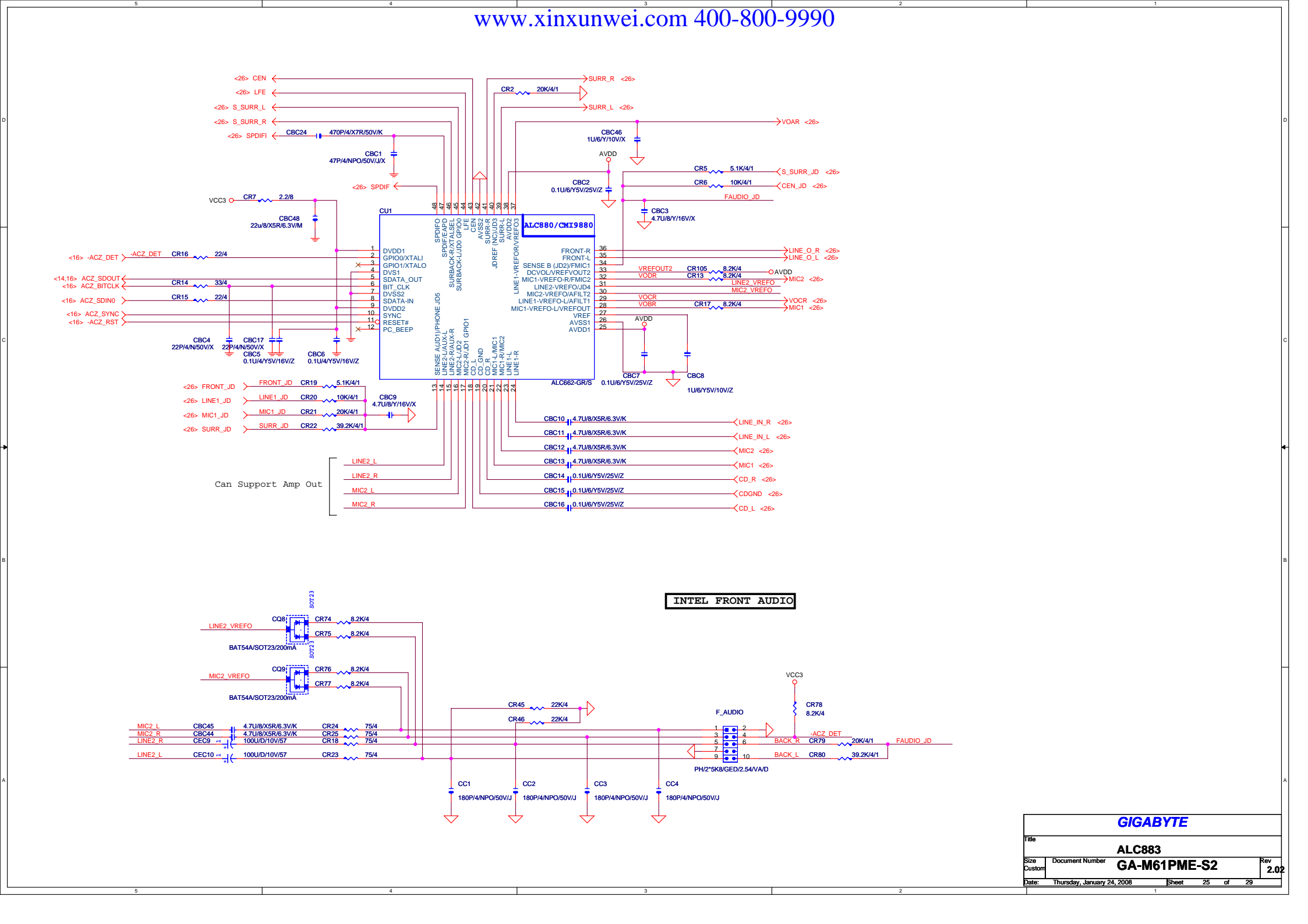


KB & MS





F USB1**F USB2****R USB****1012 EMI**



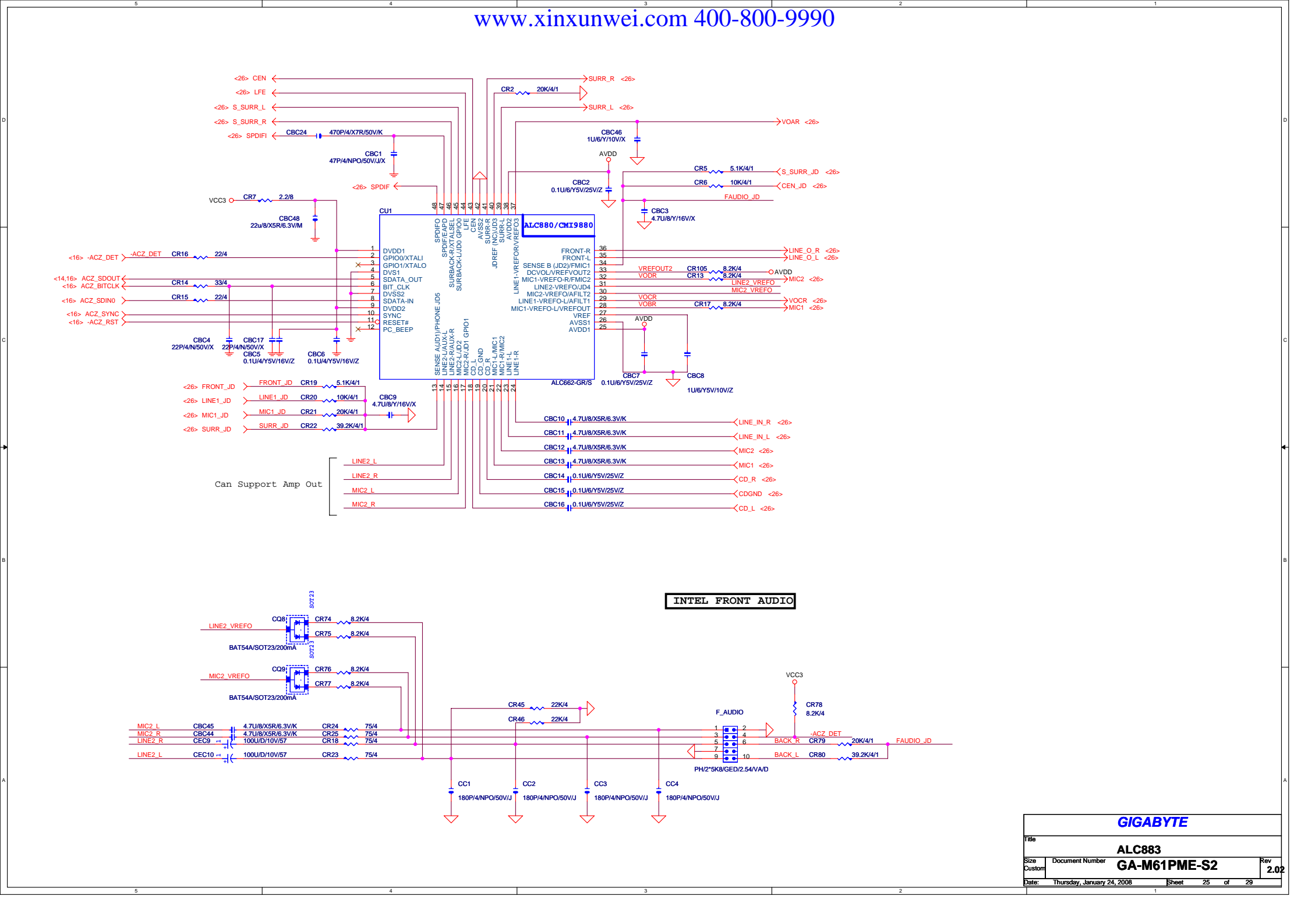
www.xinxunwei.com 400-800-9990

Can Support Amp Out

INTEL FRONT AUDIO

Component	Value
CBC1	470P/4/X7R/50V/K
CBC2	0.1U/6/Y5V/25V/Z
CBC3	4.7U/8/Y/16V/X
CBC4	22P/4/N/50V/X
CBC5	0.1U/4/Y5V/16V/Z
CBC6	0.1U/4/Y5V/16V/Z
CBC7	0.1U/6/Y5V/25V/Z
CBC8	1U/6/Y5V/10V/Z
CBC9	4.7U/8/Y/16V/X
CBC10	4.7U/8/X5R/6.3V/K
CBC11	4.7U/8/X5R/6.3V/K
CBC12	4.7U/8/X5R/6.3V/K
CBC13	4.7U/8/X5R/6.3V/K
CBC14	0.1U/6/Y5V/25V/Z
CBC15	0.1U/6/Y5V/25V/Z
CBC16	0.1U/6/Y5V/25V/Z
CBC17	22P/4/N/50V/X
CBC24	470P/4/X7R/50V/K
CBC46	1U/6/Y/10V/X
CBC48	22u/8/X5R/6.3V/M
CR2	20K/4/1
CR5	5.1K/4/1
CR6	10K/4/1
CR7	2.2/8
CR14	33/4
CR15	22/4
CR16	22/4
CR17	8.2K/4
CR19	5.1K/4/1
CR20	10K/4/1
CR21	20K/4/1
CR22	39.2K/4/1
CR23	75/4
CR24	75/4
CR25	75/4
CR18	75/4
CR74	8.2K/4
CR75	8.2K/4
CR76	8.2K/4
CR77	8.2K/4
CR45	22K/4
CR46	22K/4
CR78	8.2K/4
CR79	20K/4/1
CR80	39.2K/4/1

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Can Support Amp Out

INTEL FRONT AUDIO

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Can Support Amp Out

INTEL FRONT AUDIO

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File: ALC883

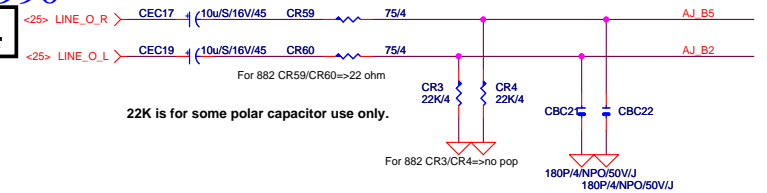
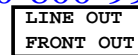
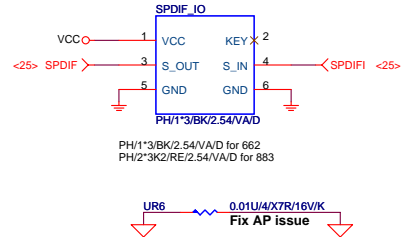
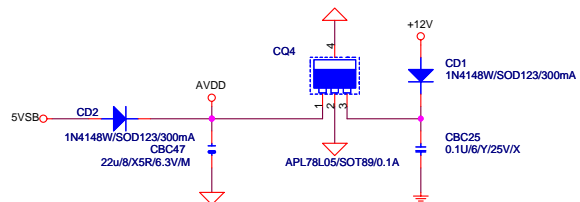
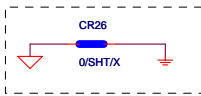
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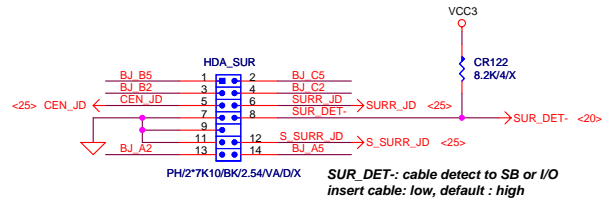
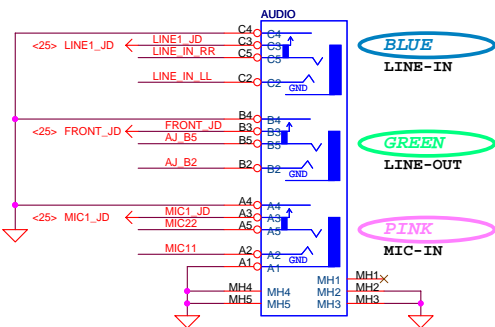
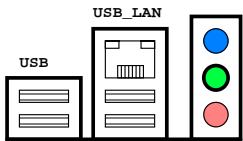
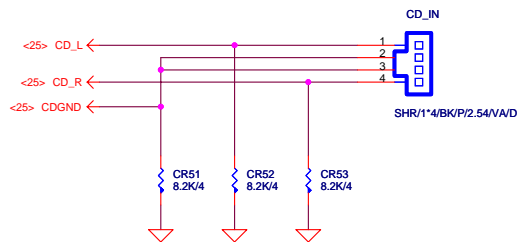
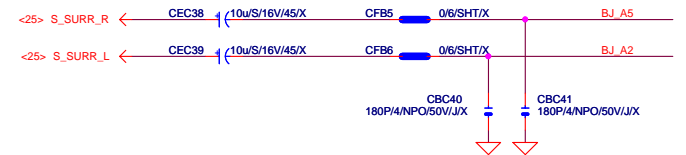
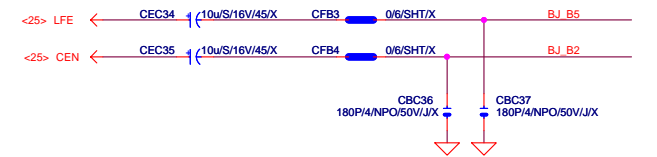
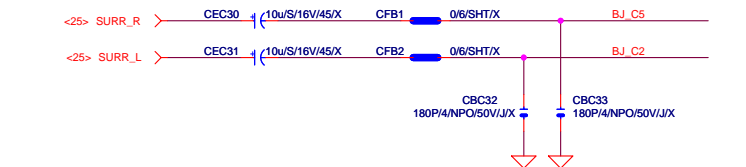
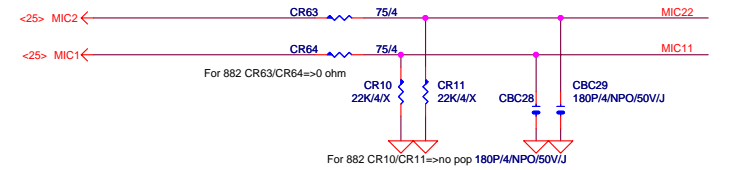
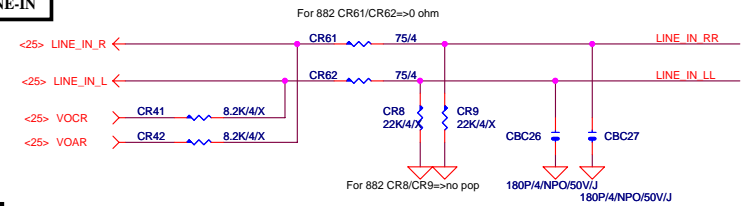
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22K is for some polar capacitor use only.



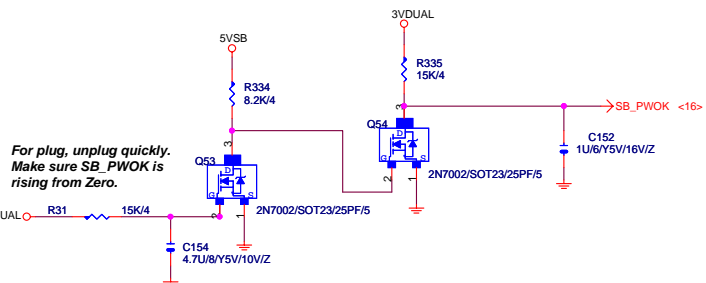
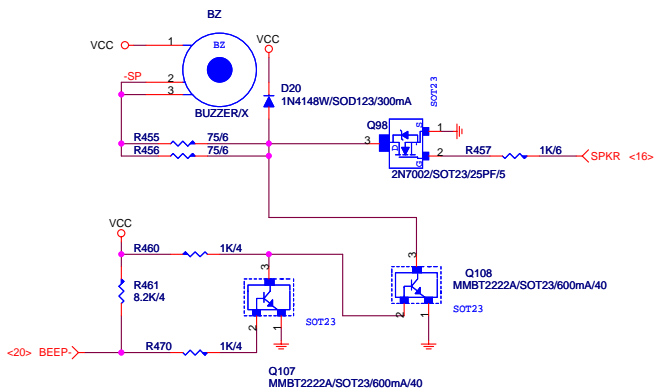
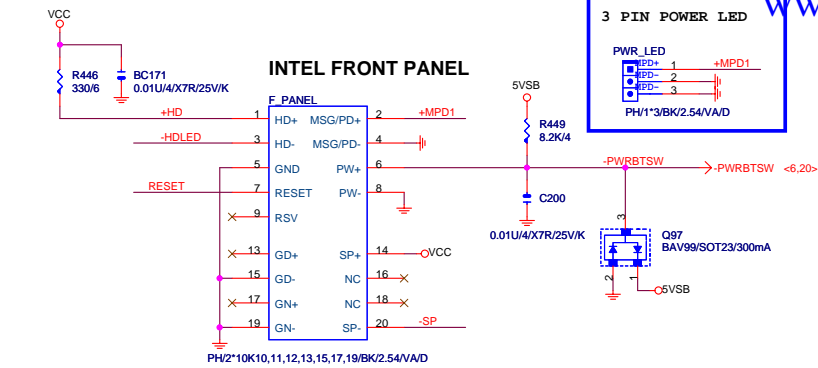
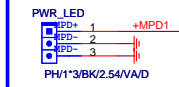
SUR_DET-: cable detect to SB or I/O
insert cable: low, default : high

A3RP/13P/BU,GE,PK/RA/D/1/A/[11NR6-403007-71R_11NR6-403007-72R_11NR6-403007-73R_11NR6-403007-74R]

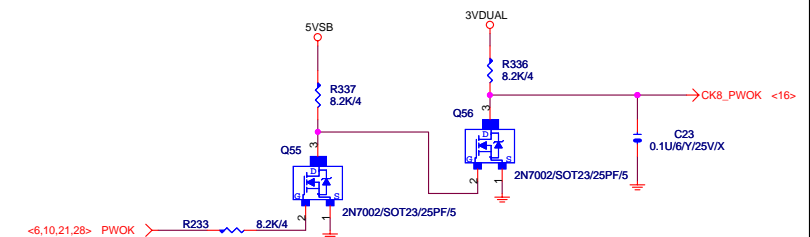
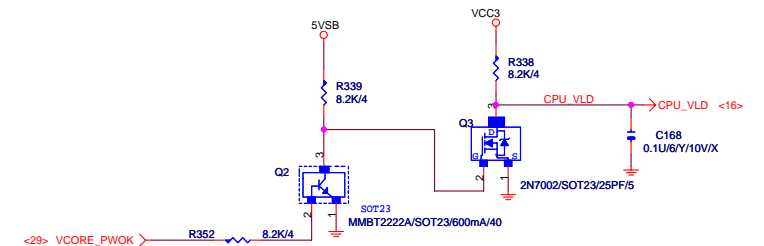
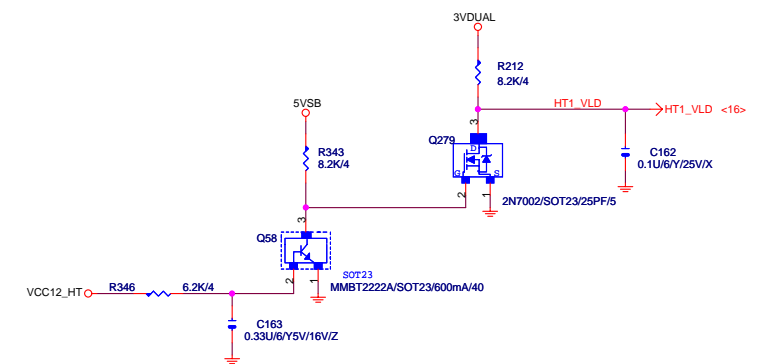
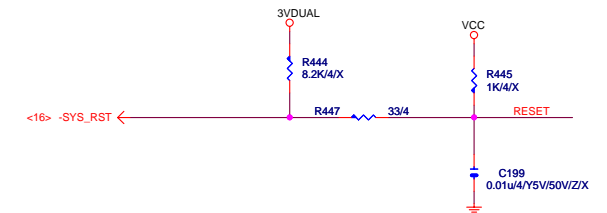
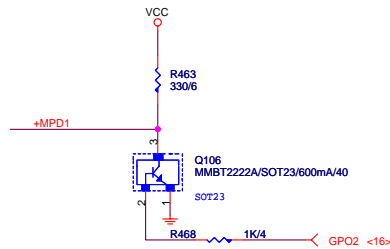
A3RJ/13P/B/[11NR6-403006-01_11NR6-403006-02]
3RJ+15F/[11NR6-403004-11]

INTEL FRONT PANEL

3 PIN POWER LED



For plug, unplug quickly.
Make sure SB_PWOK is
rising from Zero.



GIGABYTE

Title		
PANEL & BUZZER		
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Custom	GA-M61PME-S2	2.02
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ATX POWER CONNECTOR

